



XA-9472

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Response
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TECHNOLOGY CENTER 2800

In re the application of:

Yasuhisa SHIMAZAKI et al.

Appln. No.: 09/855,660

Group Art Unit: 2819

Filed: May 16, 2001

Examiner: D. Chang

For: SEMICONDUCTOR INTEGRATED CIRCUIT

* * *

RESPONSE

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Responding to the Office Action mailed March 6, 2002,
and to the requirement for restriction therein, Applicants
elect to prosecute in this application the invention
designated as Group I (Claims 1-24 and 37-42). Applicants
reserve the right to file a divisional patent application
with regard to the non-elected invention.

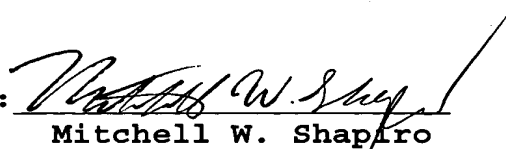
An action on the merits of the claims to the elected
invention is respectfully requested.

The Commissioner is hereby authorized to charge to
Deposit Account No. 50-1165 any fees under 37 C.F.R. §§
1.16 and 1.17 which may be required by this paper, and to

credit any overpayment to that Account. If any extension of time is required in connection with the filing of this paper and has not been requested separately, then such extension is hereby requested.

Respectfully submitted,

By:


Mitchell W. Shapiro
Reg. No. 31,568

MWS:jab

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March 29, 2002